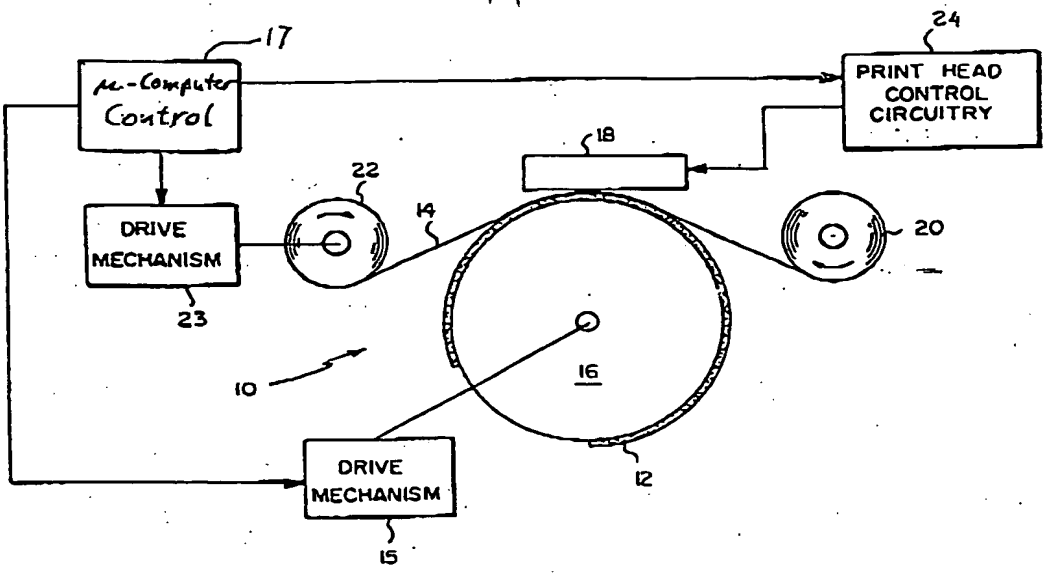

FIG. 1

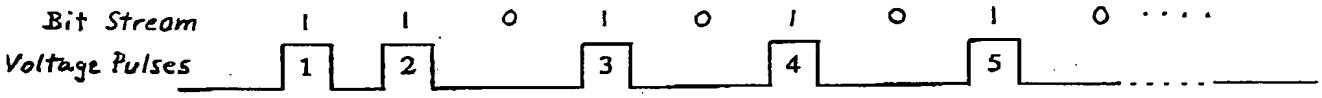

Figure 2

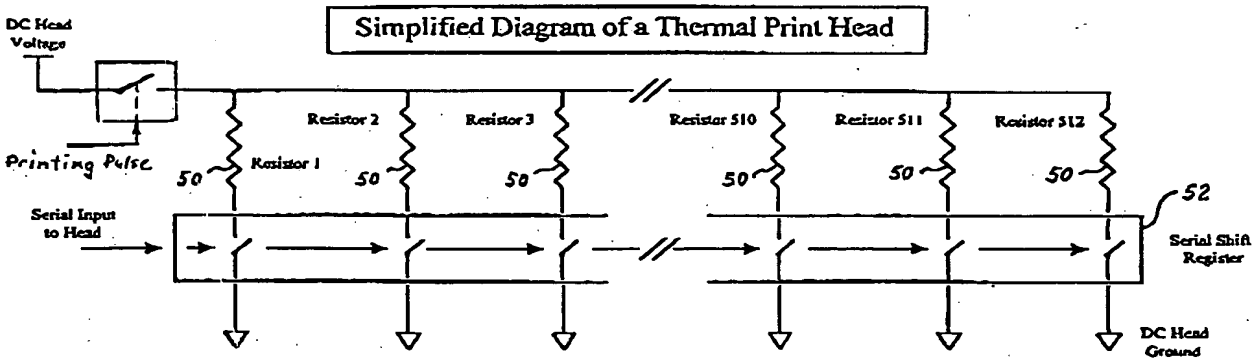

Figure 3

Table III

	Generating Modulation Bits by Addition Method							
Complement	63	62	61	60	59	58	57	...
Pixel Value	5	5	5	5	5	5	5	...
Sum	68	67	66	65	64	63	62	...
Sum(7 bit binary)	1000100	1000011	1000010	1000001	1000000	0111111	0111110	...
Modulation bit	1	1	1	1	1	0	0	...

Fig. 4

Table IV

	Rearranging Complement Values to Scatter the Modulation bits							
Complement	63	58	61	57	62	60	59	...
Pixel Value	5	5	5	5	5	5	5	...
Sum	68	63	66	62	67	65	64	...
Sum(7 bit binary)	1000100	0111111	1000010	0111110	1000011	1000001	1000000	...
Modulation bit	1	0	1	0	1	1	1	...

Fig. 5

Table V

	Repeating Values in the Complement Table to Provide Non-Linear Response							
Complement	63	63	63	60	59	58	57	...
Pixel Value	5	5	5	5	5	5	5	...
Sum	68	68	68	65	64	63	62	...
Sum(7 bit binary)	1000100	1000100	1000100	1000001	1000000	0111111	0111110	...
Modulation bit	1	1	1	1	1	0	0	...

Fig. 6

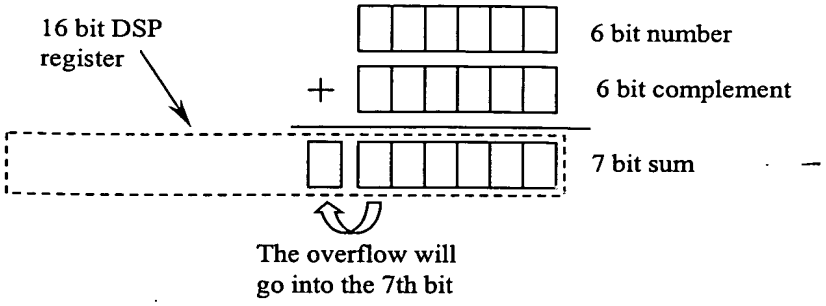

Figure 7

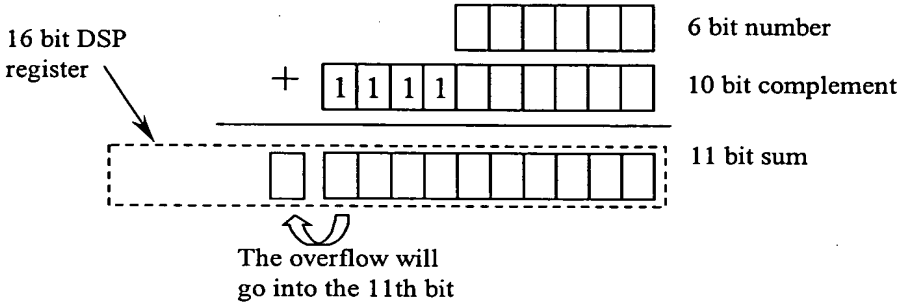

Figure 8

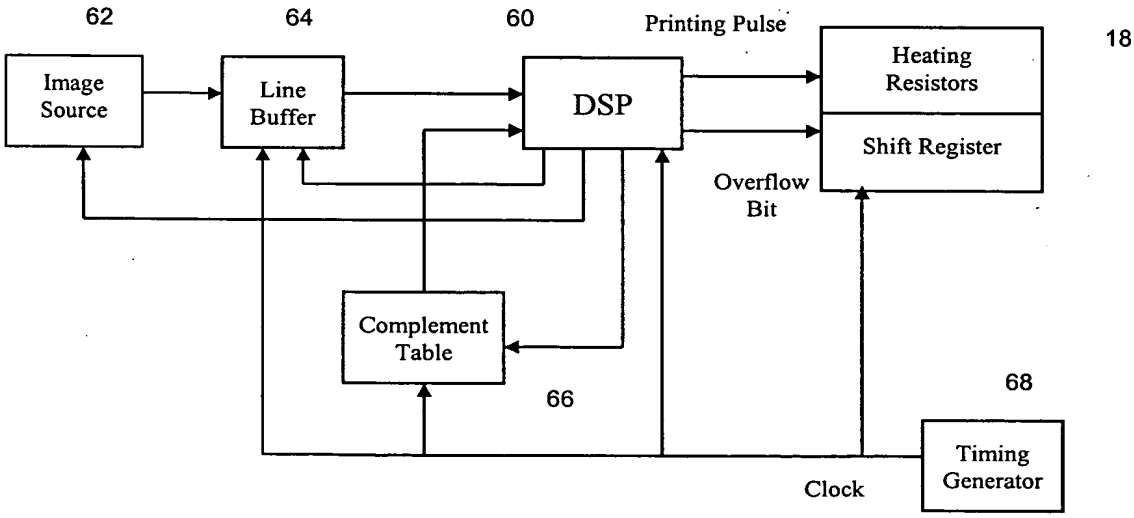

Figure 10

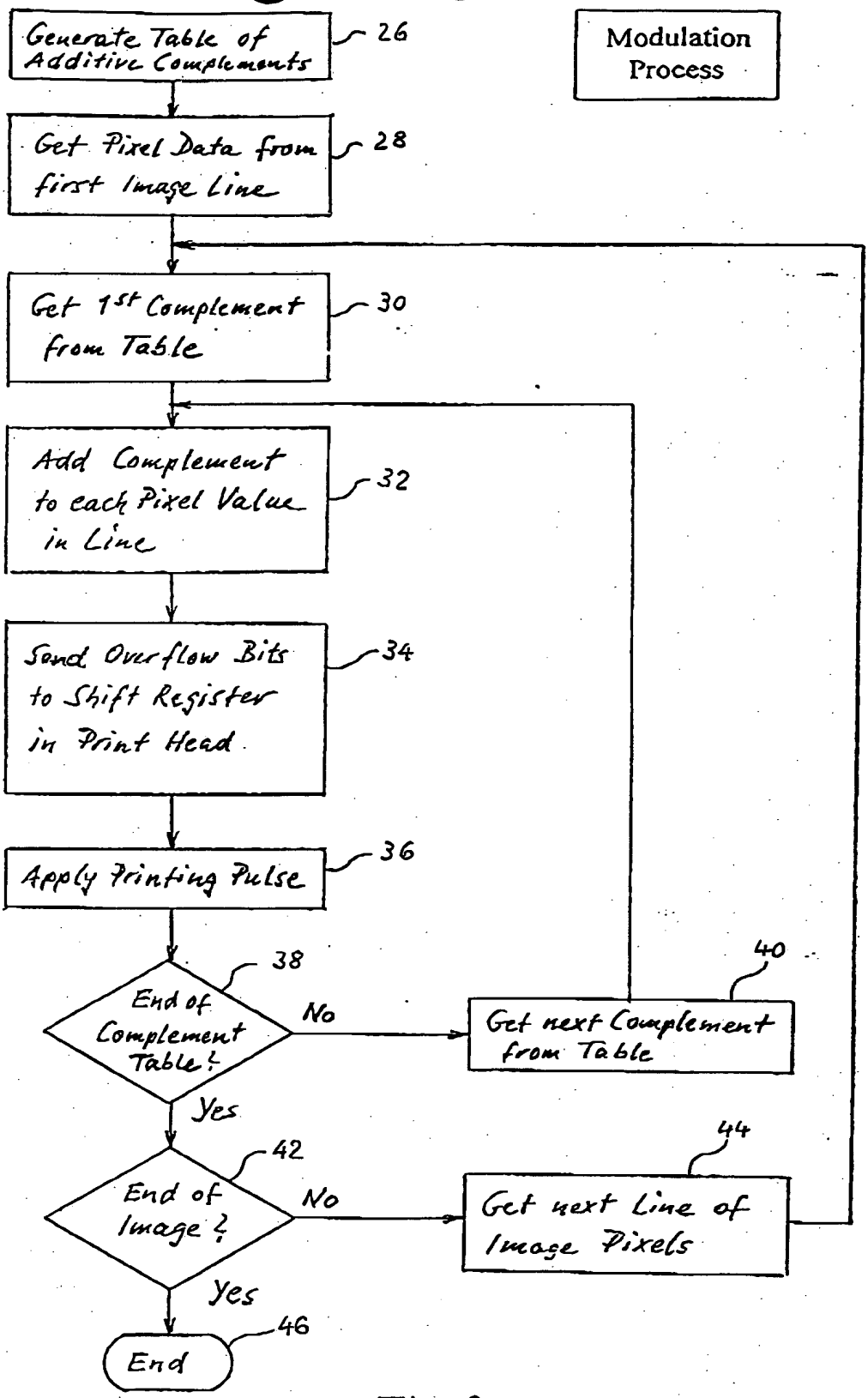
Modulation Process
Generate Table of Additive Complements
26
Get Pixel Data from first Image Line
28
Get 1st Complement from Table
30
Add Complement to each Pixel Value in Line
32
Send Overflow Bits to Shift Register in Print Head
34
Apply Printing Pulse
36
End of Complement Table?
38
No
Get next Complement from Table
40
Yes
End of Image?
42
No
Get next Line of Image Pixels
44
Yes
End
46

Fig. 9